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20575 7590 10/12/2005

MARGER JOHNSON & MCCOLLOM, P.C.
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LI Mei Vermilya

(Depositor's name)

(Signature)

January 12, 2006

(Date)

01/13/2006 MAHMED2 00000105 10773805

01 FC:1501 1400.00 OP
02 FC:1504 1400.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/773,805	02/05/2004	Sang-Eun Lee	8750-057	8451

TITLE OF INVENTION: METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT USING A SELECTIVE DISPOSABLE SPACER TECHNIQUE AND SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURED THEREBY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	01/12/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
GEBREMARIAM, SAMUEL A	2811	438-230000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Samsung Electronics Co., Ltd. Suwon-si, Gyeonggi-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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 A check in the amount of the fee(s) is enclosed.
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 b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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PTO-85 (Rev. 07/05) Approved for use through 04/30/2007.

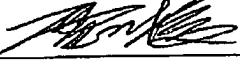
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I hereby certify that the attached Issue Fee Transmittal with PTO Form 85B, PTO Form 2038 authorizing credit card payment of \$1,700.00 for the Issue (\$1,400.00) and Publication fees (\$300.00), are submitted to the U.S. Patent and Trademark Office via facsimile number (571) 273-2885 on the date shown below. (Total 3 pages).

Date: January 12, 2006


Li Mei Vermilya

PATENT APPLICATION
Attorney Docket No.: 8750-057
Client Ref. No.: SPX200303-0009US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sang-Eun LEE and Yun-Heub SONG

Serial No.:	10/773,805	Examiner:	Gebremariam, Samuel A.
Filed:	February 5, 2004	Group Art Unit:	2811
Confirmation No.:	8451		
For:	METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT USING A SELECTIVE DISPOSABLE SPACER TECHNIQUE AND SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURED THEREBY		

TRANSMITTAL LETTER

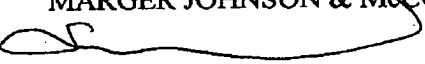
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Enclosed for filing in the above-referenced application are the following:

- Publication (\$300.00) and Issue (\$1400.00 large) fees
- In connection with issuance of a patent:
 - Supplemental Declaration PTO Form 85B
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Customer No. 20575

Respectfully submitted,
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